

#2112
T. Young
4-21-97

JC530 U.S. PTO
09/229628

01/13/99

Application Branch

Applicants : Yasutaka SAKAINO et al.
Appln. No. : To be Assigned
Filed : January 13, 1999
For : SEMICONDUCTOR INTEGRATED
CIRCUIT
Atty. Dkt. : OKI 4646.01

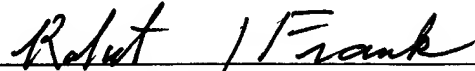
INFORMATION DISCLOSURE STATEMENT

January 13, 1999

As all requirements of 37 CFR 1.97 ff and all official guidelines pertaining to Information Disclosure Statements,

ave been complied with, it is respectfully requested that the Examiner consider the reference and make it of record.

Respectfully submitted,



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